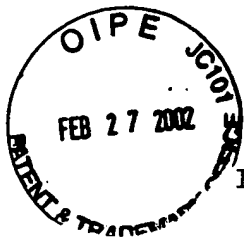


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PATENT #3/29/02



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In re application : John V. Reynders  
Application No. : 10/027,886  
Filed : October 19, 2001  
For : EFFICIENT SYSTEM AND METHOD OF NODE AND  
LINK INSERTION FOR DEADLOCK-FREE ROUTING  
ON ARBITRARY  
Examiner : Not Assigned  
Attorney's Docket : SUNM-058XX

Group Art Unit: 2181

\*\*\*\*\*  
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By: David A. Dagg  
David A. Dagg  
Registration No. 37,809  
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Sir:

In reviewing the Filing Receipt (copy enclosed) for the  
above-identified patent application, the following error was  
noted:

Efficient system and method of node and link insertion for  
deadlock-free routing on **arbitrary**

Application No.: 10/027,886  
Filed: October 19, 2001  
Group Art Unit: 2181

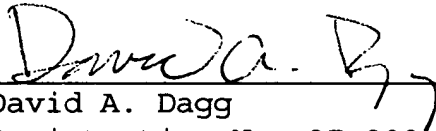
should read:

Efficient system and method of node and link insertion for  
deadlock-free routing on **arbitrary**

Kindly correct the error of the Patent and Trademark Office  
and issue a corrected Filing Receipt.

Respectfully submitted,

By:

  
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Enclosure

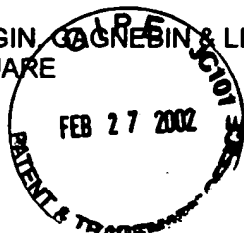
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
10/027,886	10/19/2001	2181	1196	P6270	10	22	8

207

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## Applicant(s)

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## Assignment For Published Patent Application

Sun Microsystems, Inc.;

## Domestic Priority data as claimed by applicant

## Foreign Applications

If Required, Foreign Filing License Granted 02/05/2002

Projected Publication Date: 04/24/2003

Non-Publication Request: No

Early Publication Request: No

## Title

Efficient system and method of node and link insertion for deadlock-free routing on arbitrary

*arbitrary**51*  
*4/11/03*  
*2.*

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